

## VITA

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**Publications**

- [1] Y. Sillapawicharn and Y. Kumsuwan, "An Improvement of Synchronously Rotating Reference Frame-Based Voltage Sag Detection for Voltage Sag Compensation Applications under Distorted Grid Voltages," in *Proceedings of IEEE-PEDS2011*, Singapore, Dec. 2011.
- [2] Y. Sillapawicharn and Y. Kumsuwan, "Voltage sag compensation using two three-phase voltage-fed PWM converters," in *Proceedings of Electrical Engineering/Electronics, Computer, Telecommunications and Information Technology (ECTI-CON) 2011*, pp.776-779, 17-19 May 2011.
- [3] Y. Sillapawicharn and Y. Kumsuwan, "Commutation Process of Thyristor-Based Static Transfer Switch for Voltage Sensitive Load Against Voltage Sags," in *Proceedings of EECN-34*, 30 Nov-2 Dec, pp. 257-260, (in Thai), 2011.
- [4] Y. Sillapawicharn and Y. Kumsuwan, "An Improvement in Synchronously Rotating Reference Frame-Based Voltage Sag Detection under Distorted Grid Voltages," *Journal of Electrical Engineering & Technology*, vol.8, no.6, pp.1283-1295, 2013.